

FIG. 1

2 / 2 0

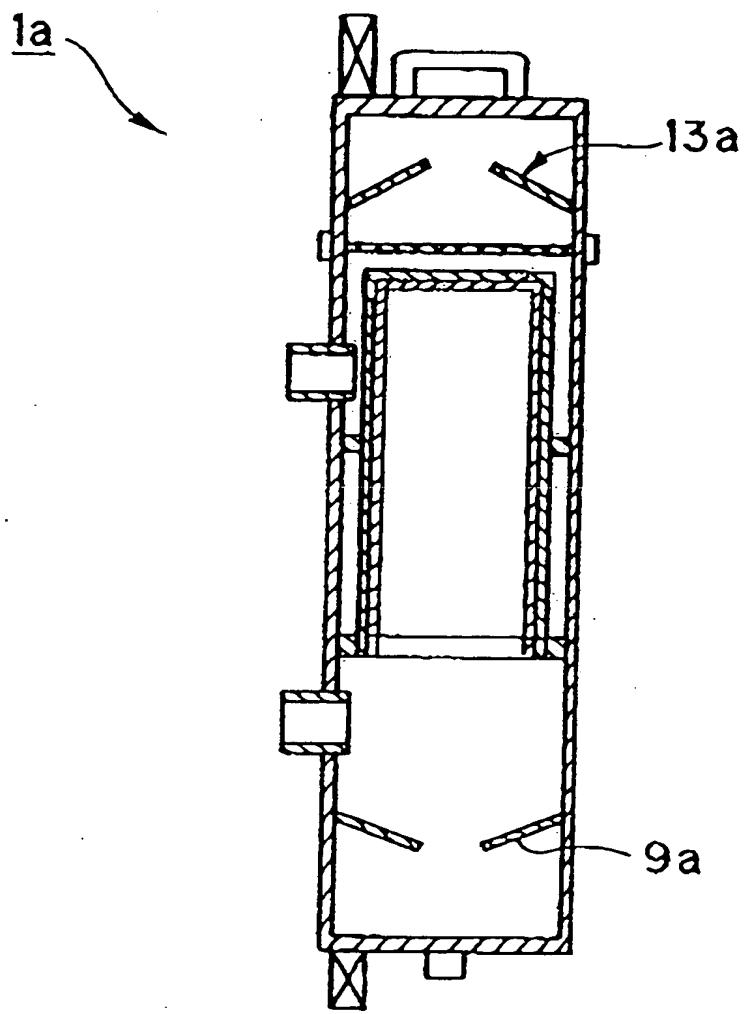


FIG. 2

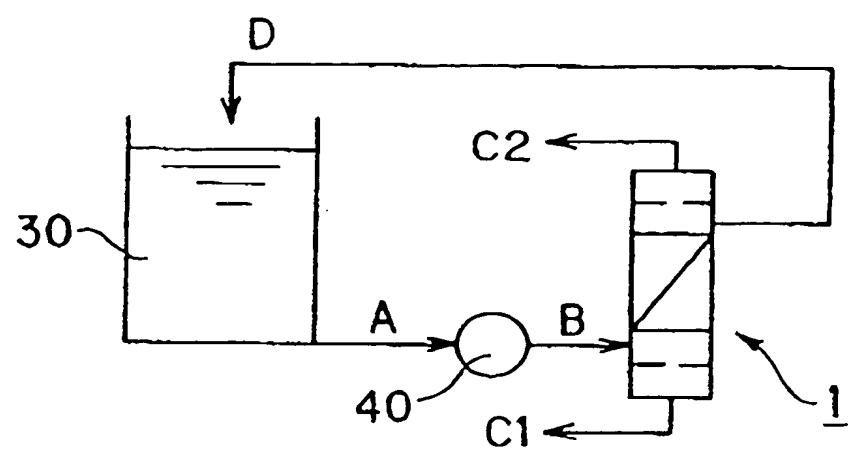


FIG. 3

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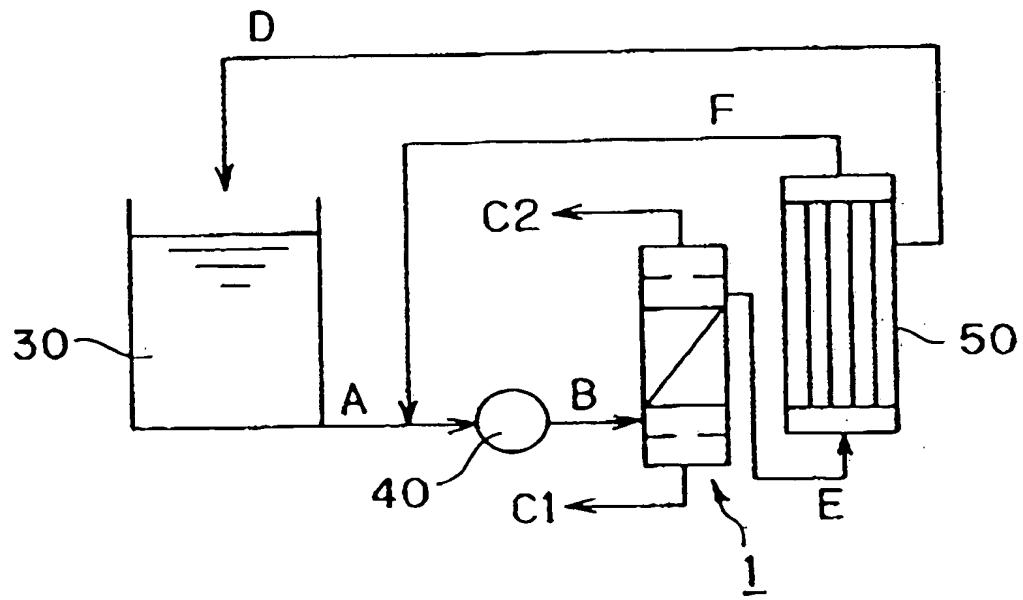


FIG. 4

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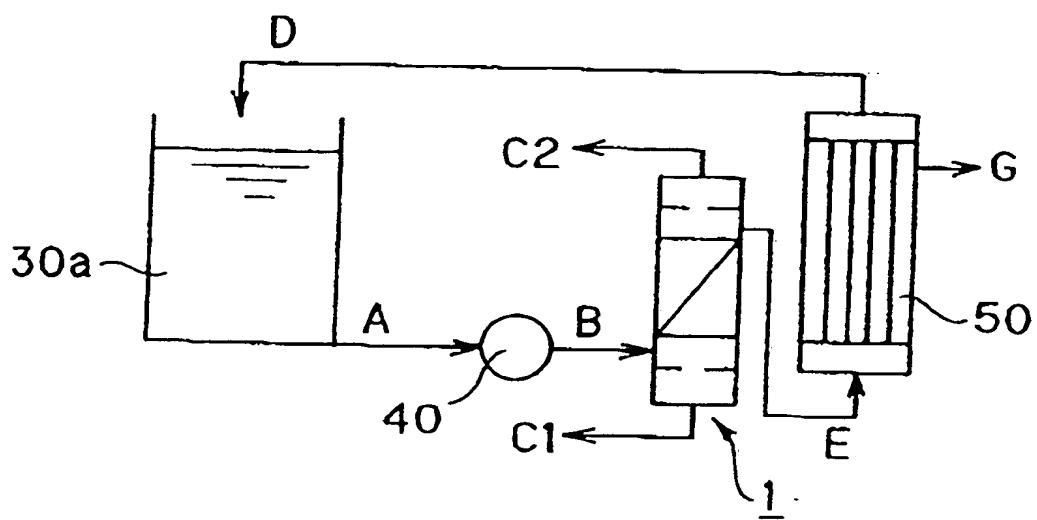


FIG. 5

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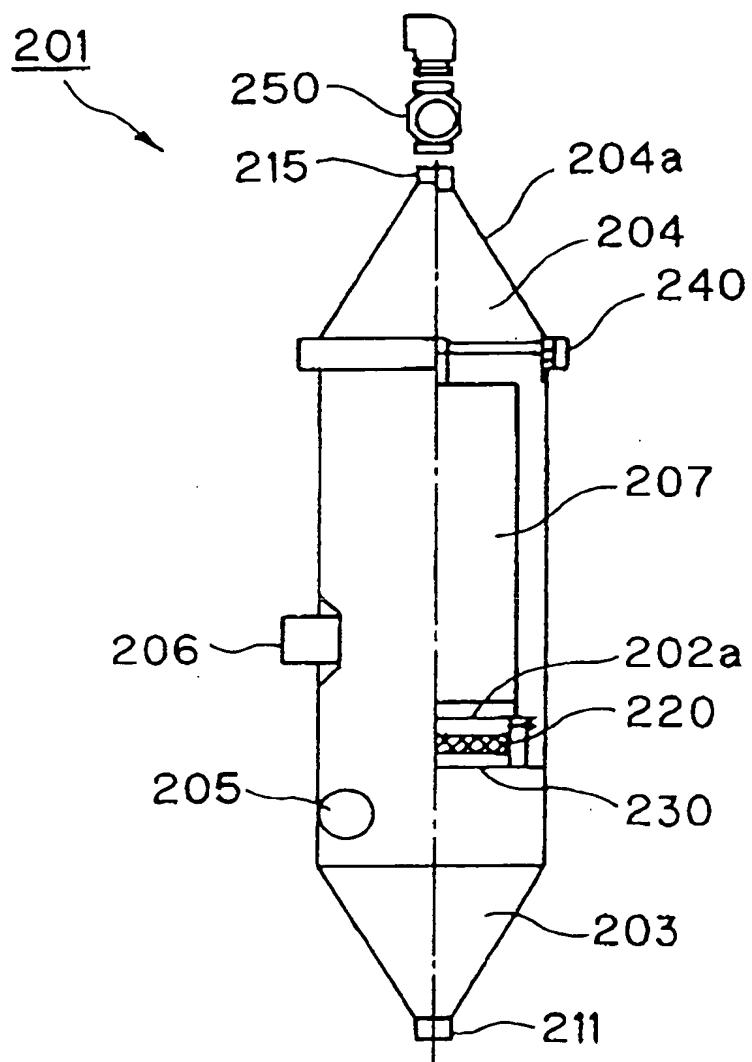


FIG. 6

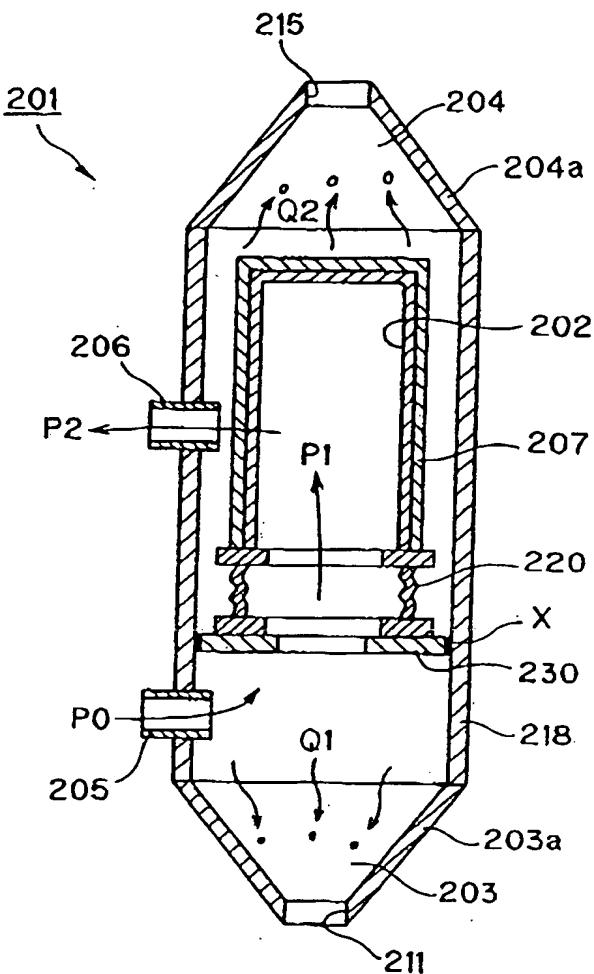


FIG. 7

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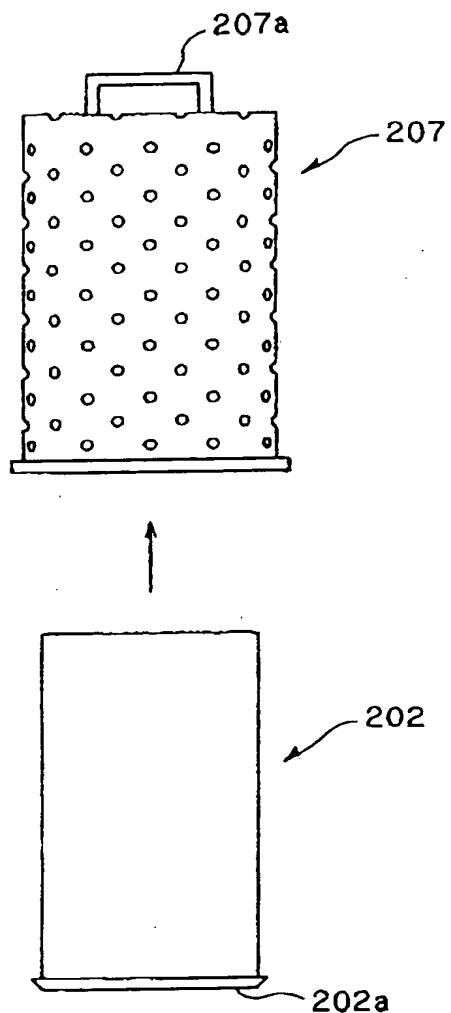


FIG. 8

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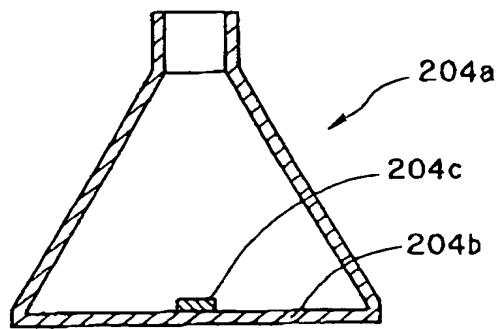


FIG. 9A

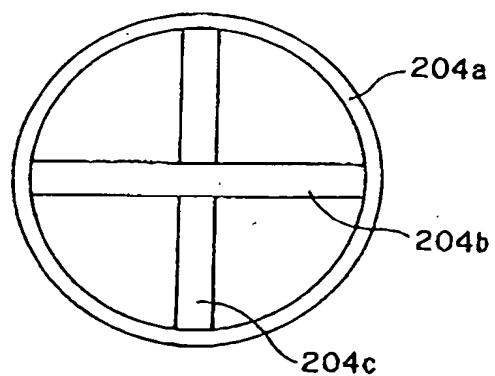


FIG. 9B

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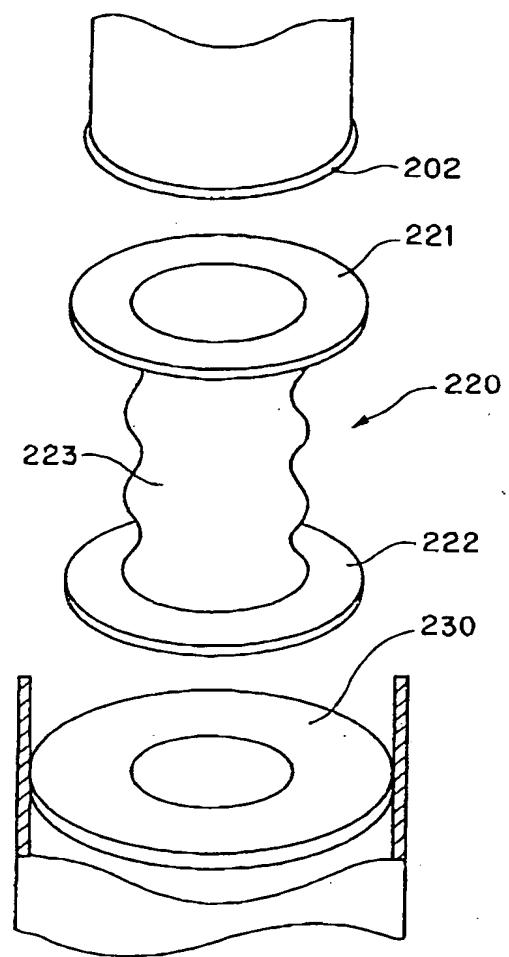


FIG. 10

1 1 / 2 0

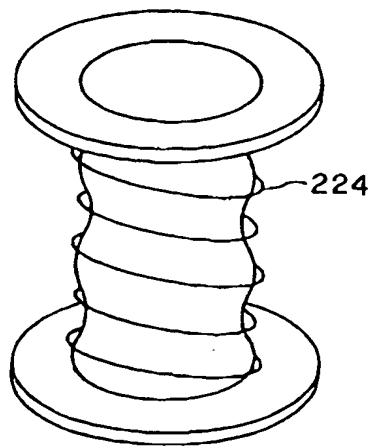


FIG. 11A

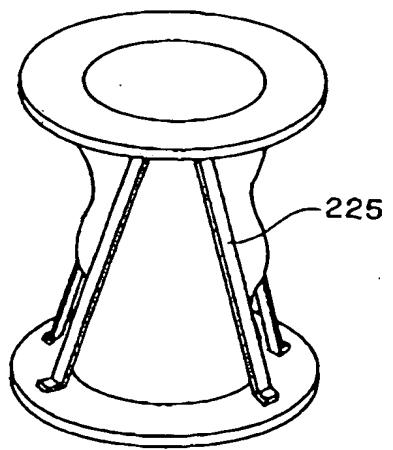


FIG. 11B

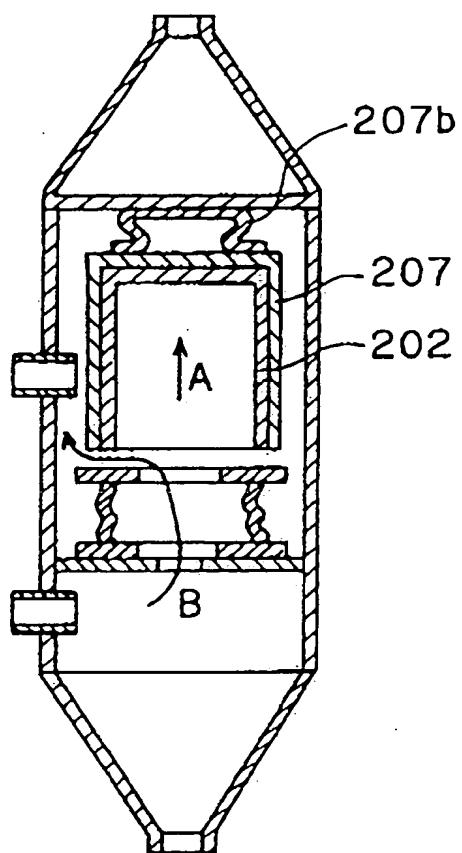


FIG. 12

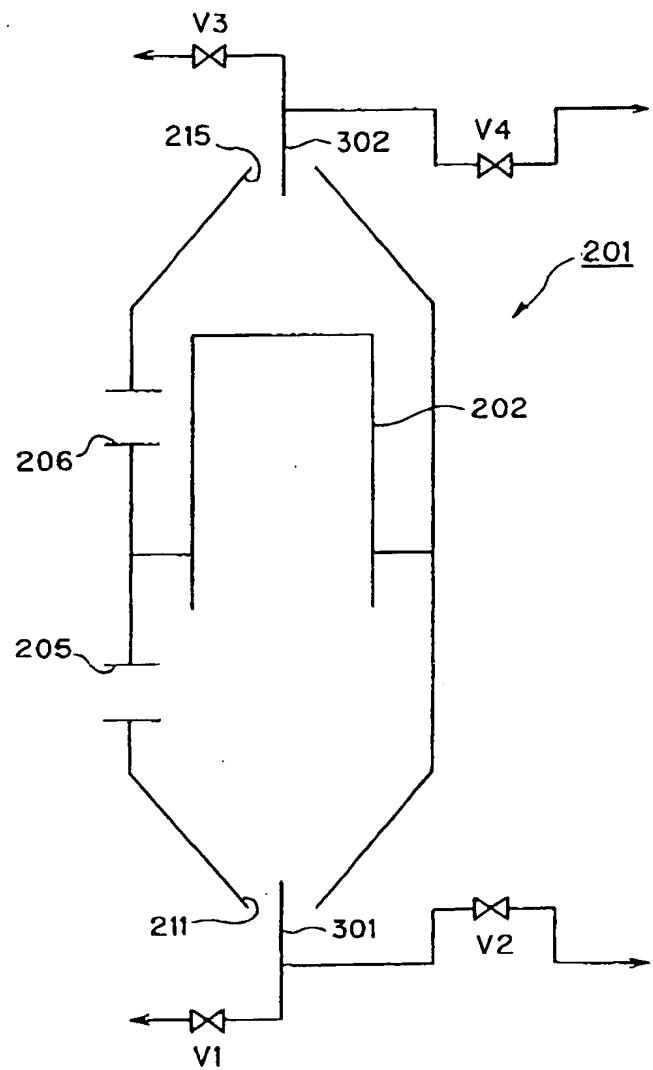


FIG. 13

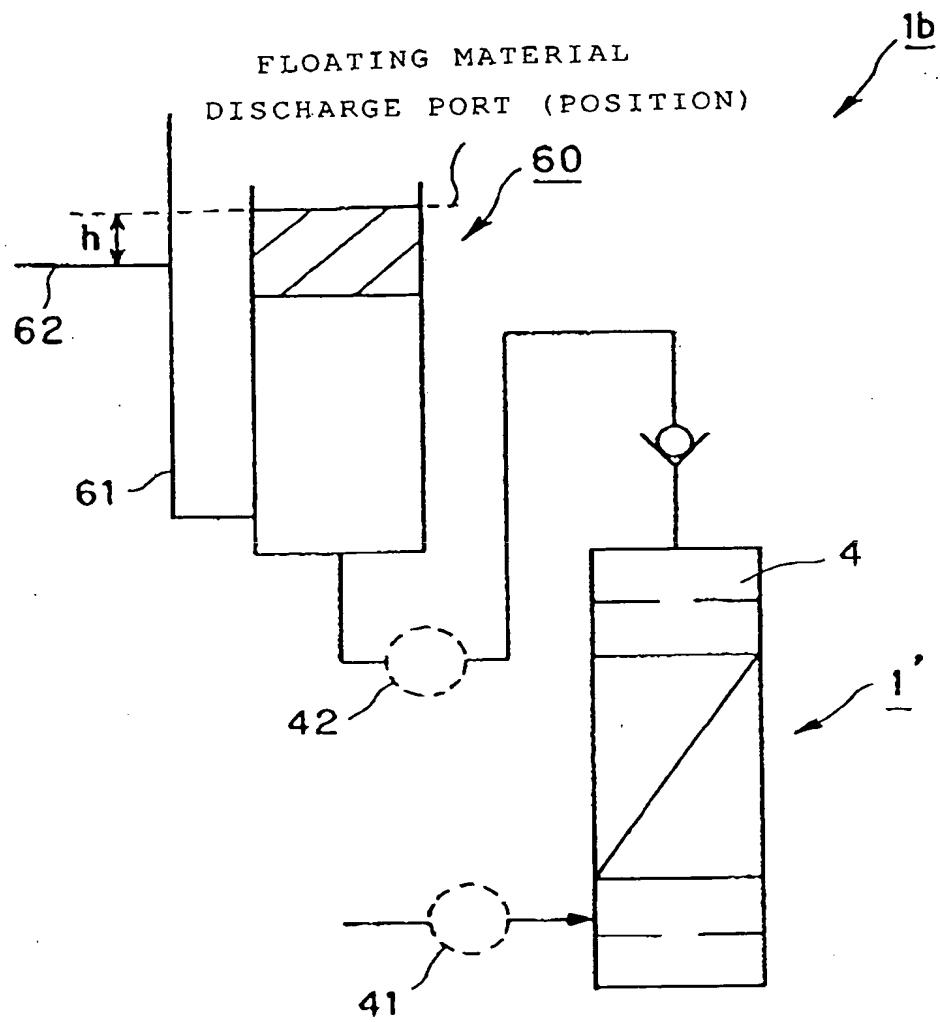


FIG. 14

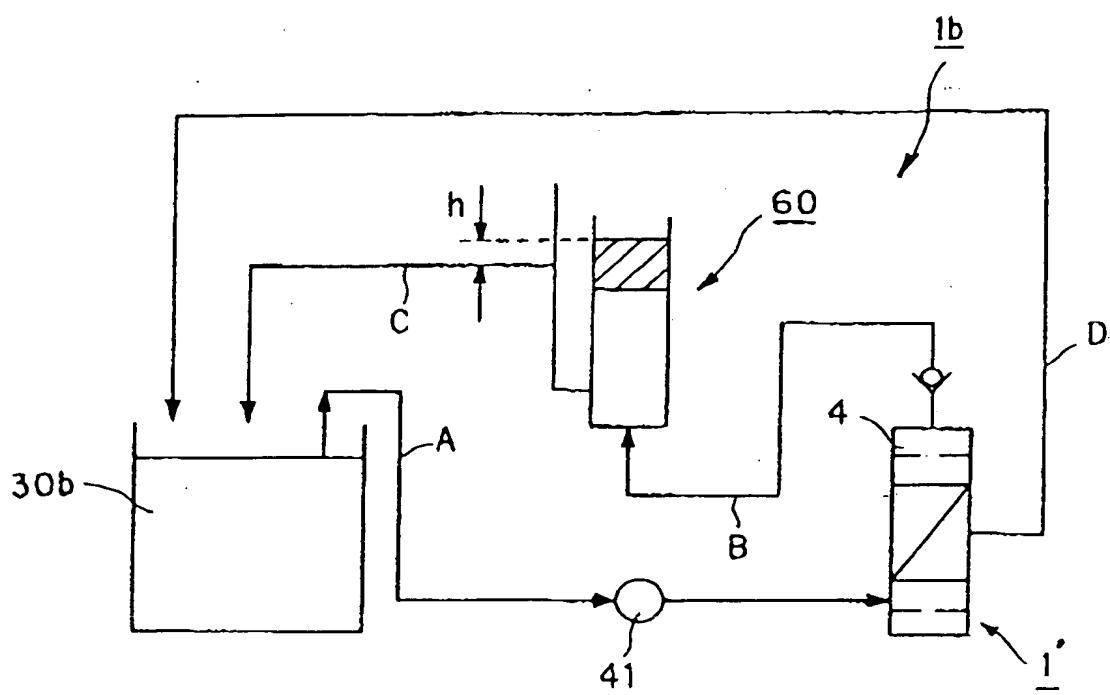


FIG. 15

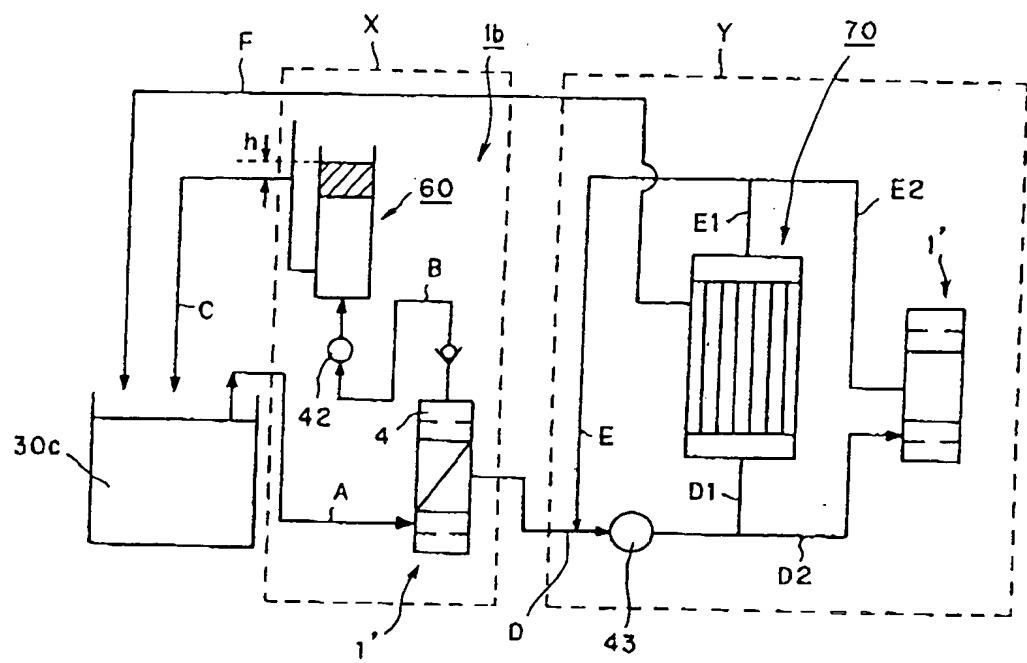


FIG. 16

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INPUT WIRING

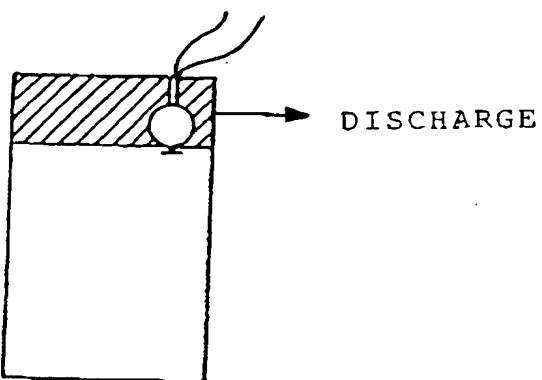


FIG. 17A

INPUT WIRING

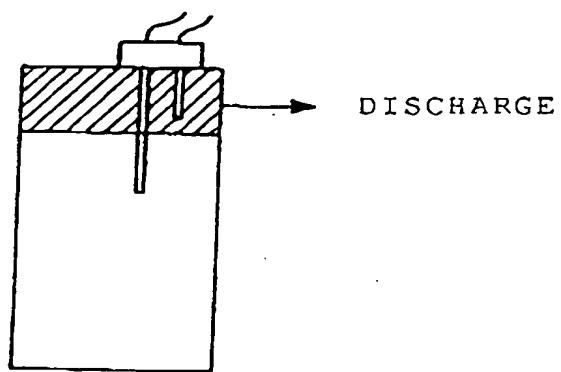


FIG. 17B

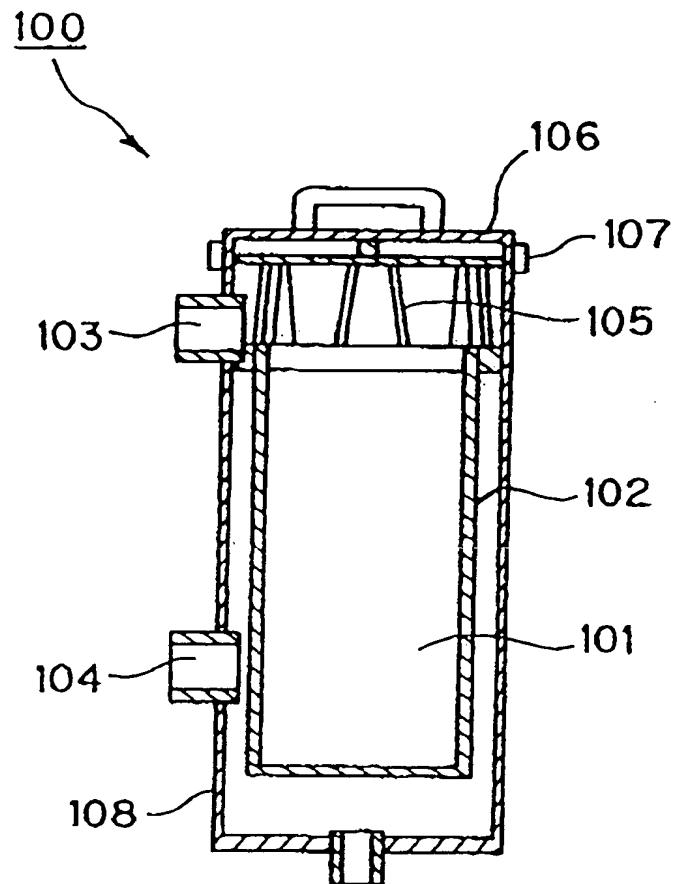


FIG. 18

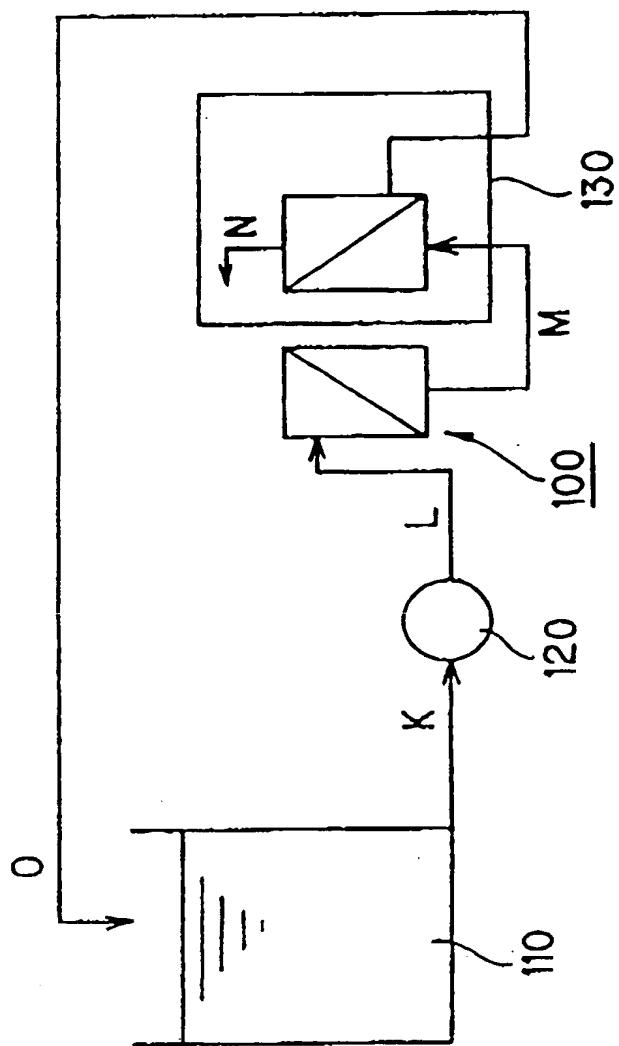


FIG. 19

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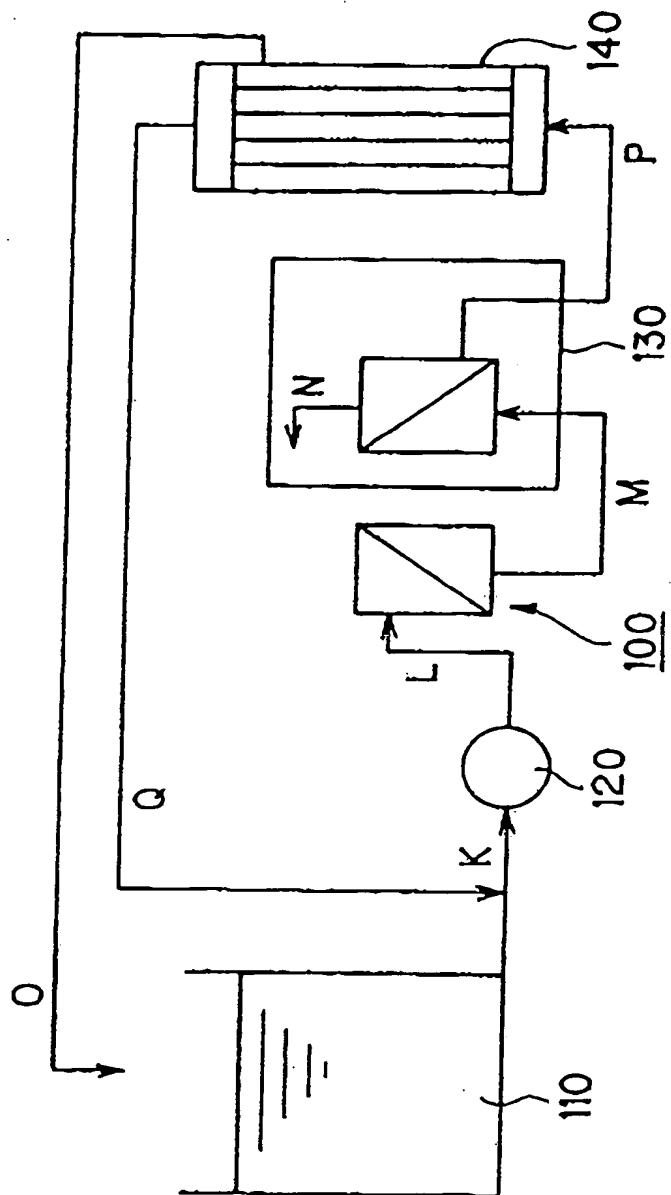


FIG. 20